

Fig. 1
(Prior Art)

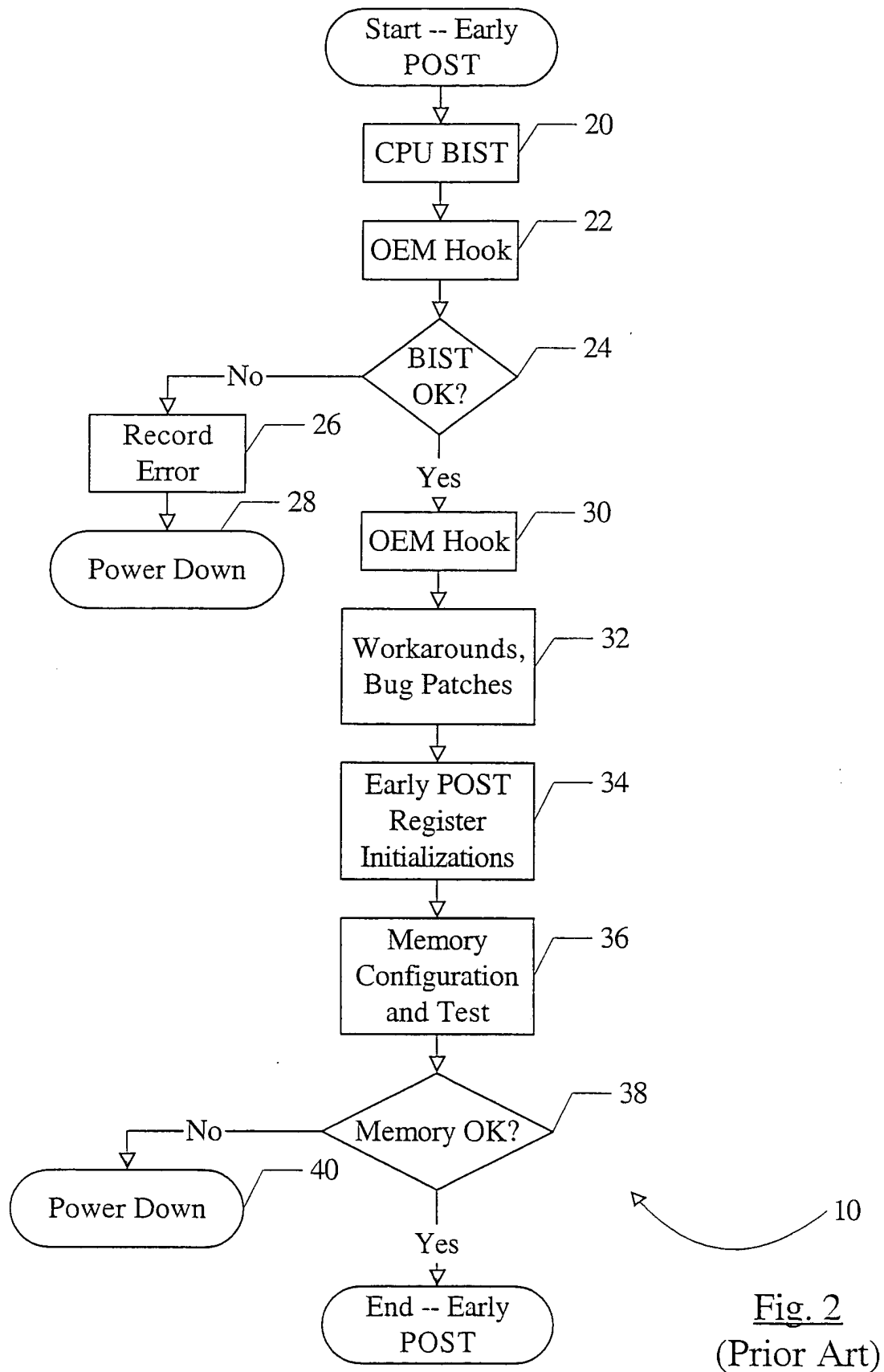


Fig. 2
(Prior Art)

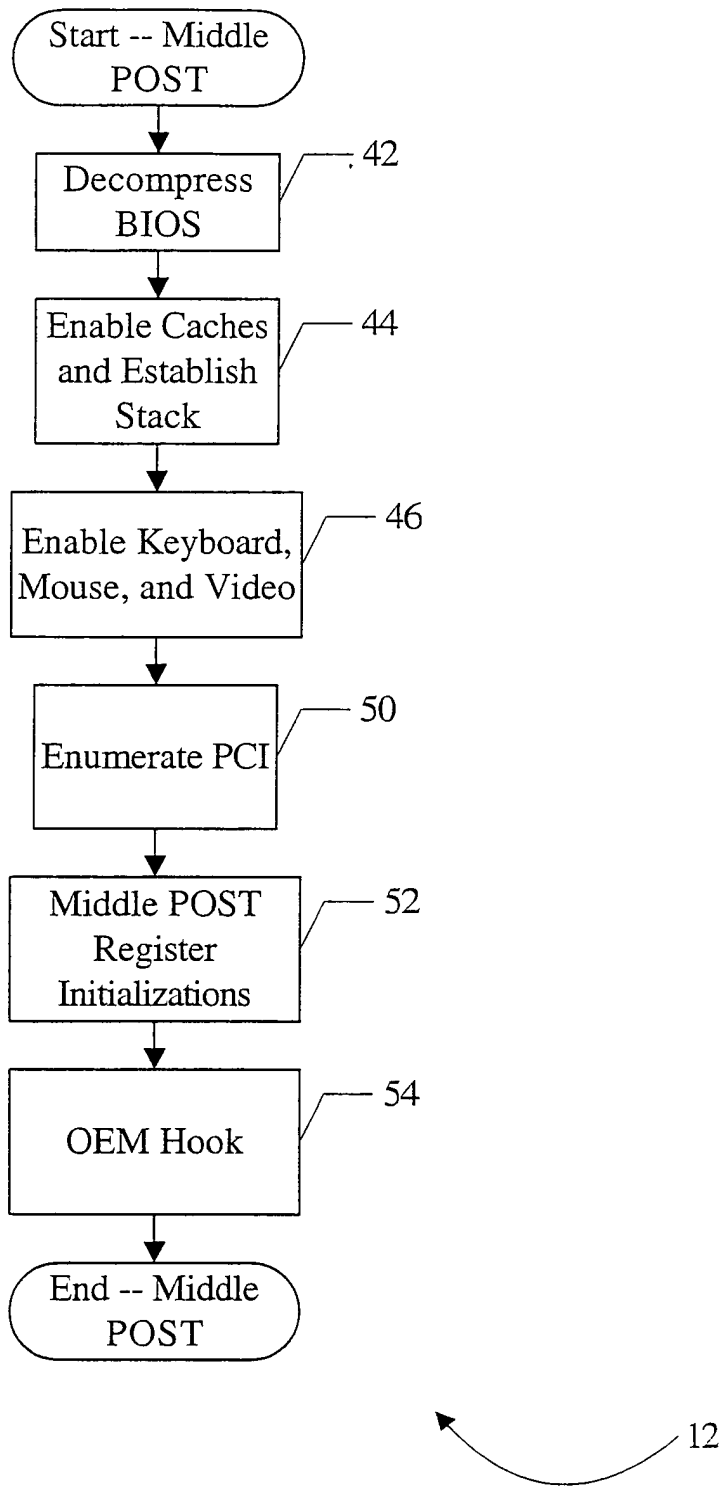


Fig. 3
(Prior Art)

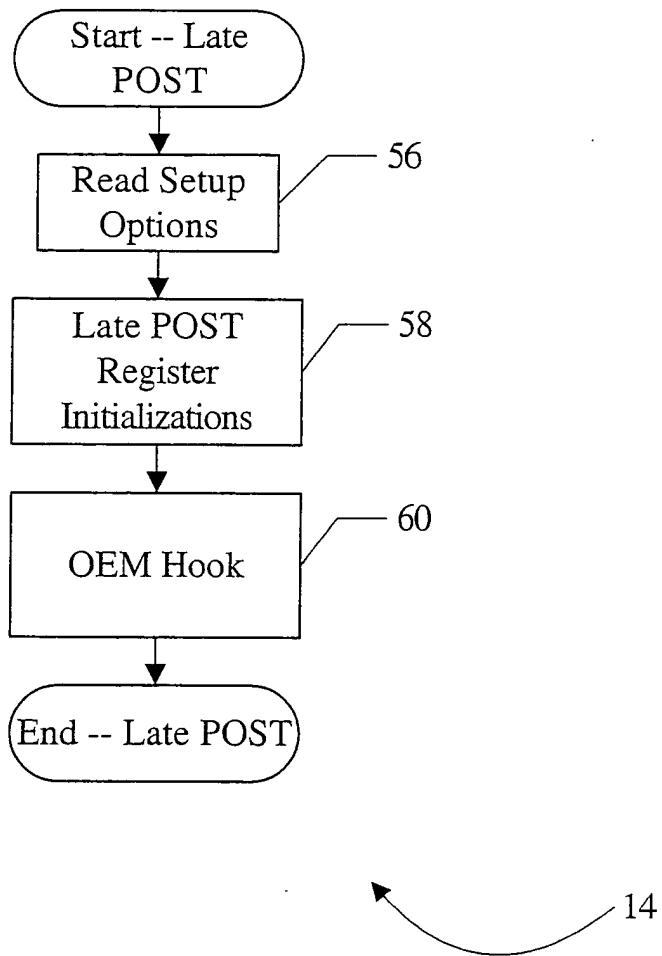


Fig. 4
(Prior Art)

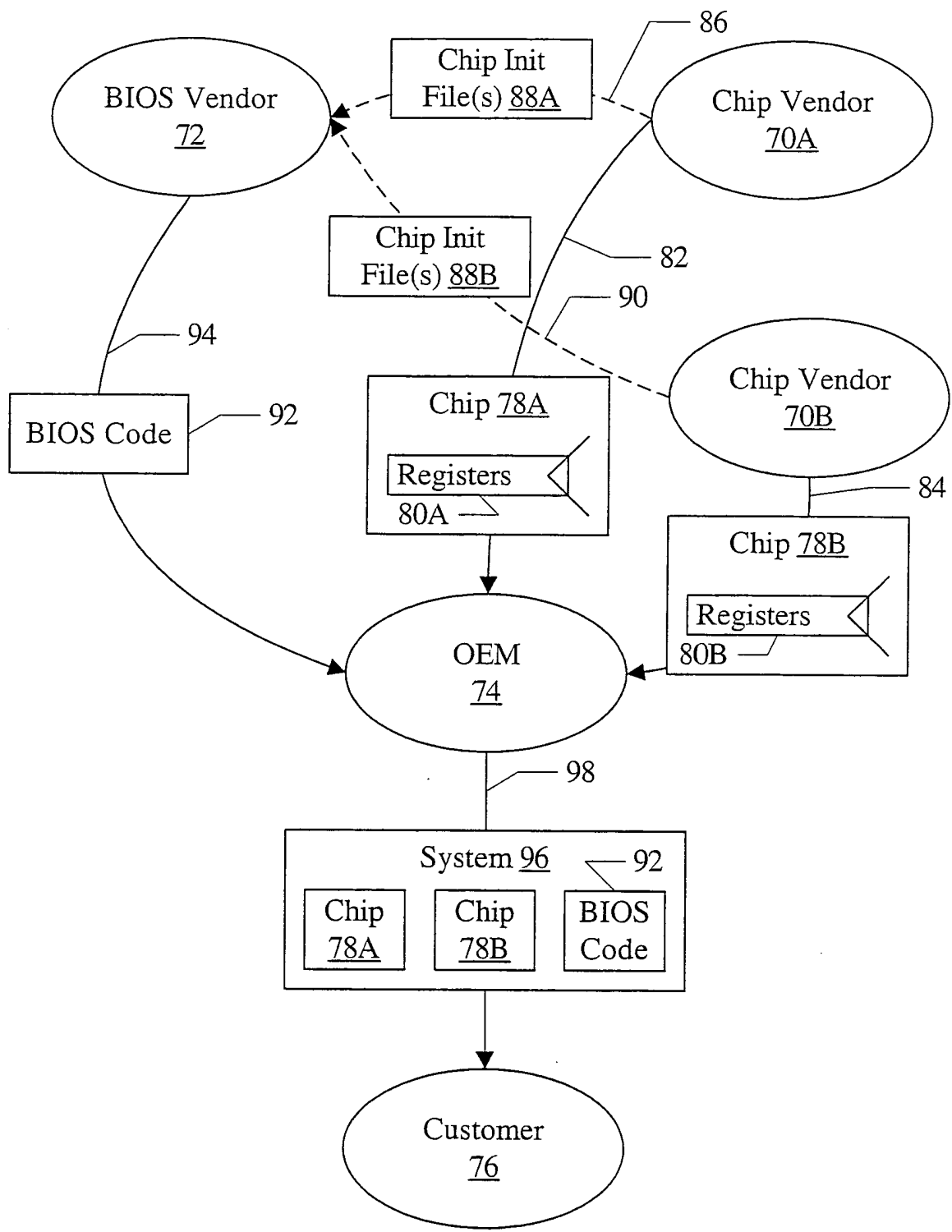


Fig. 5

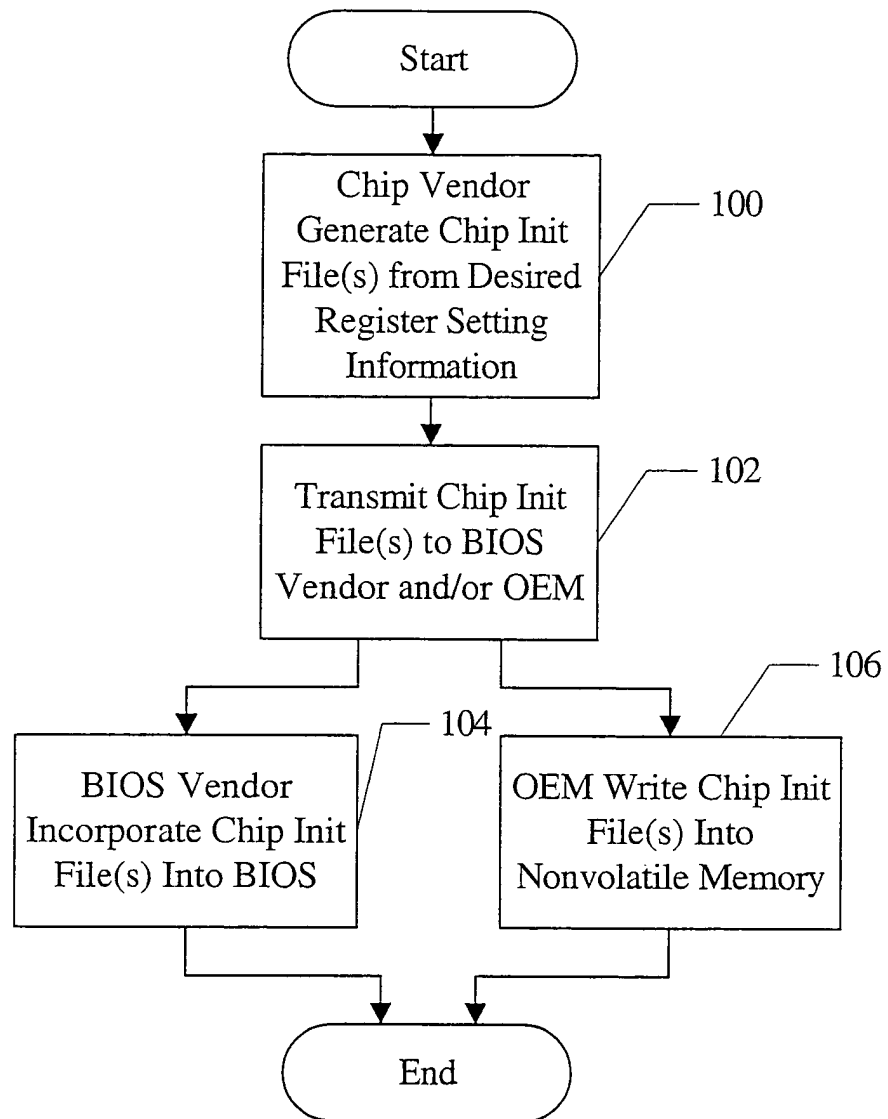


Fig. 6

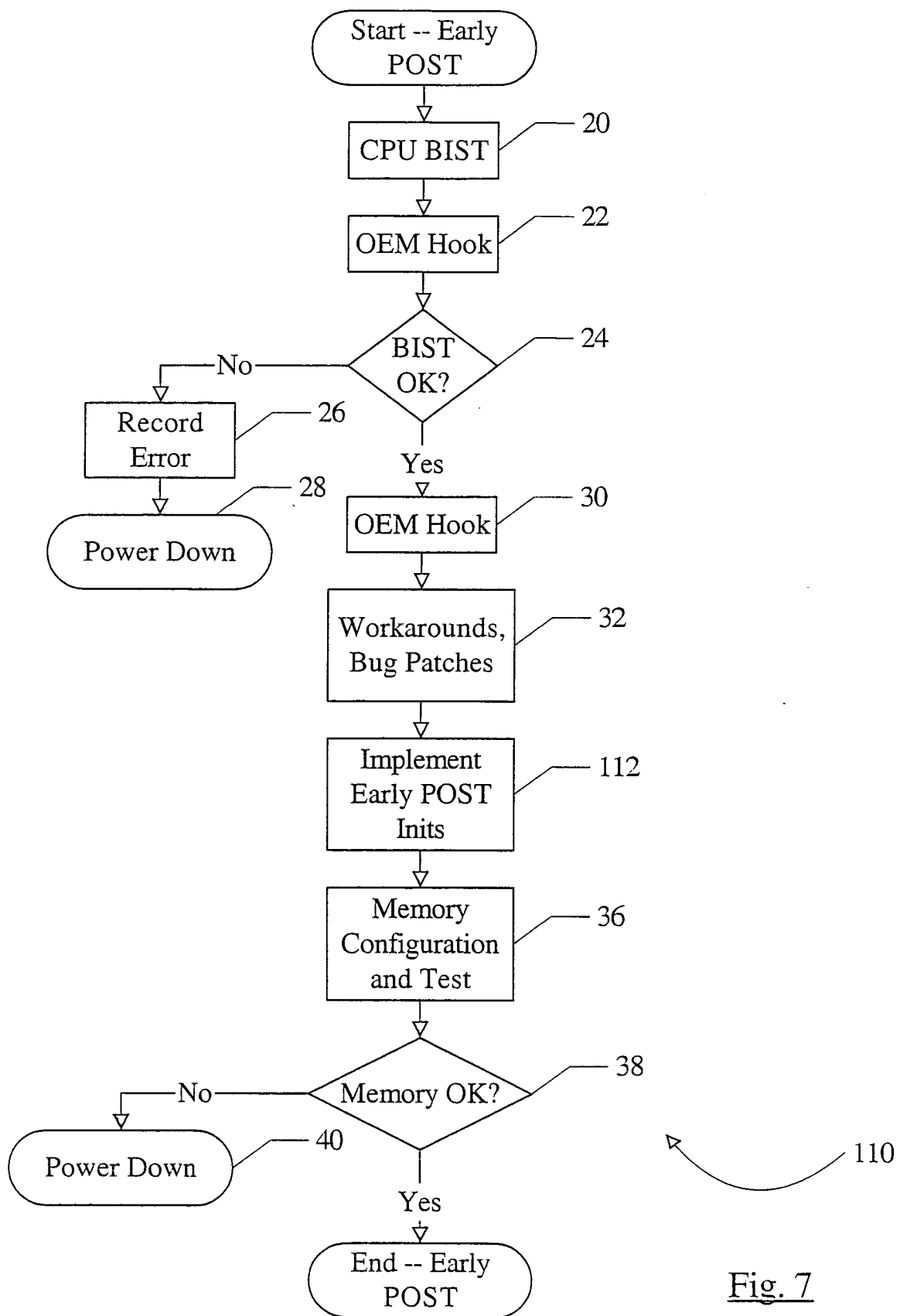


Fig. 7

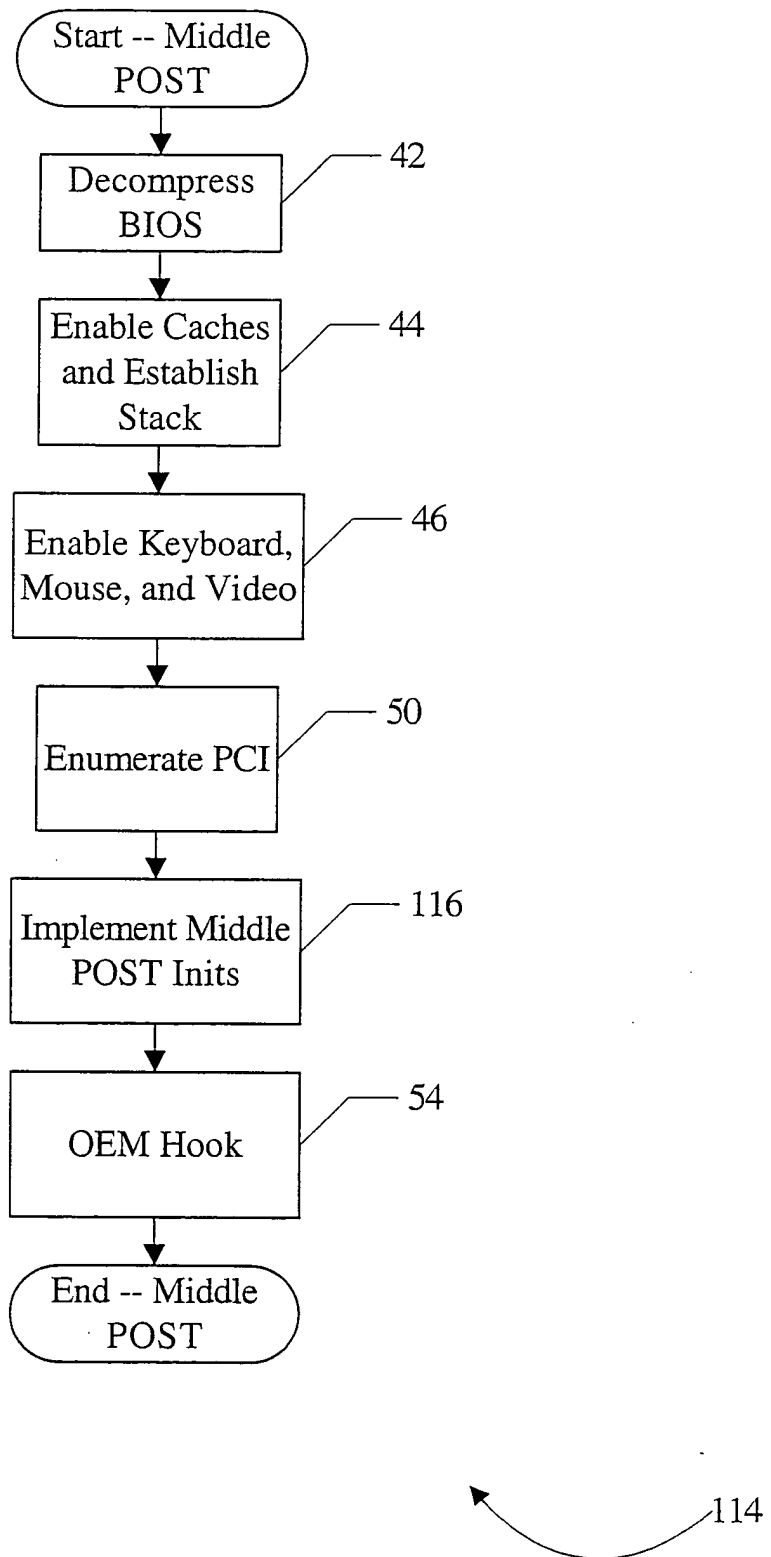


Fig. 8

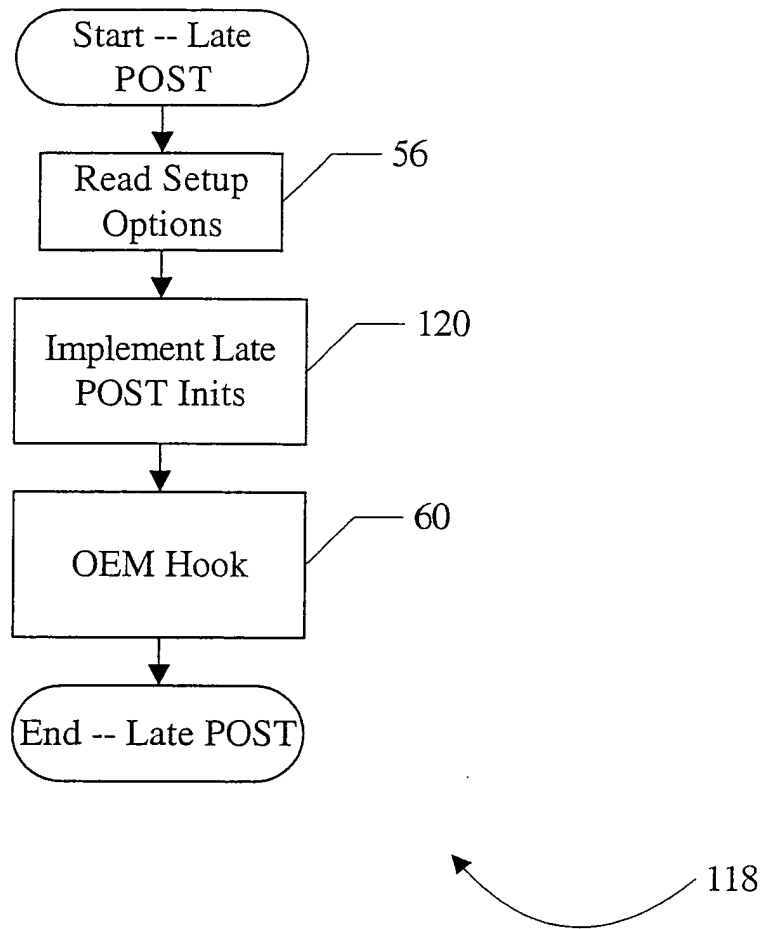


Fig. 9

Chip Init File 88A

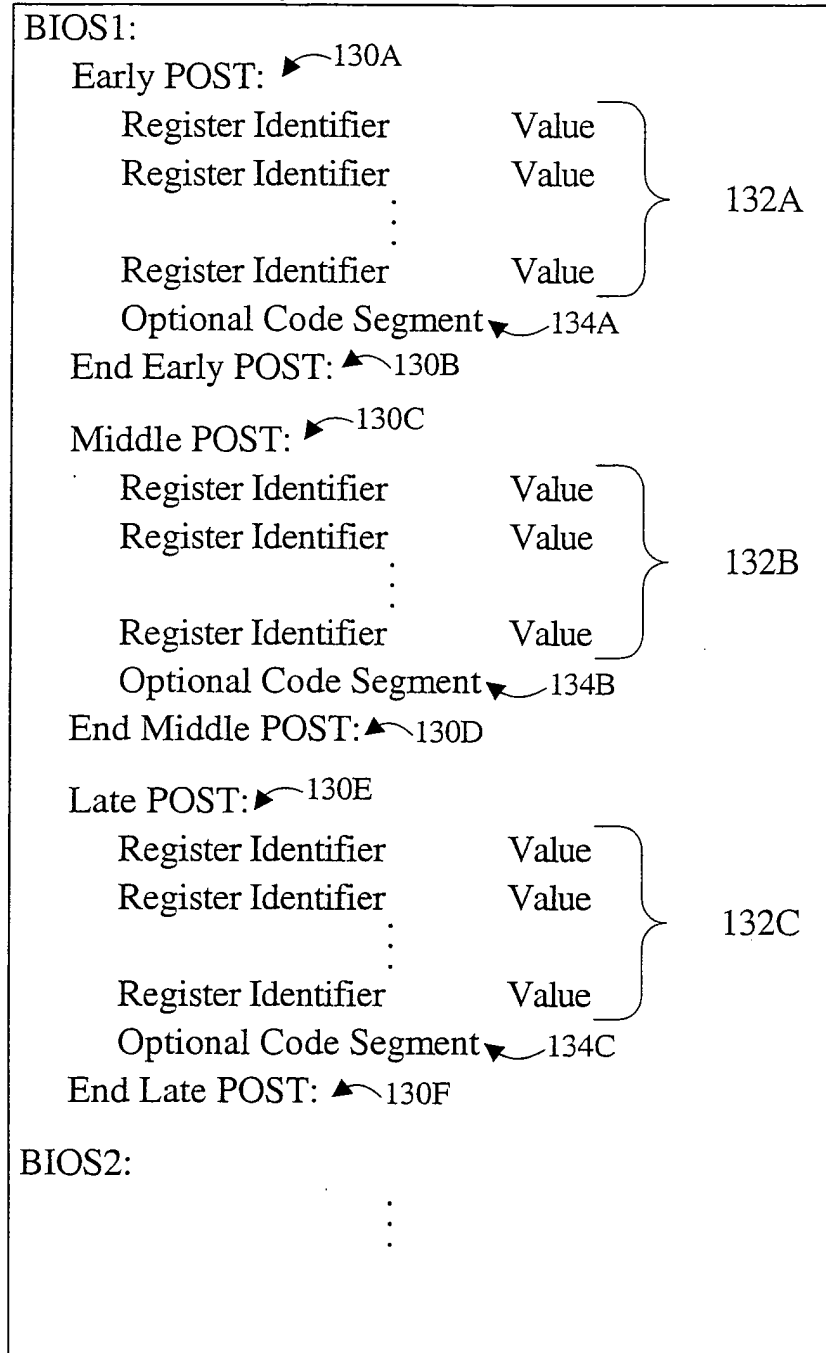


Fig. 10

Chip Init File 88A

BIOS1:

Early POST: ↘ 130A

Early Post Code Segment ↘ 136A

End Early POST: ↘ 130B

Middle POST: ↘ 130C

Middle Post Code Segment ↘ 136B

End Middle POST: ↘ 130D

Late POST: ↘ 130E

Late Post Code Segment ↘ 136C

End Late POST: ↘ 130F

BIOS2:

⋮

Fig. 11

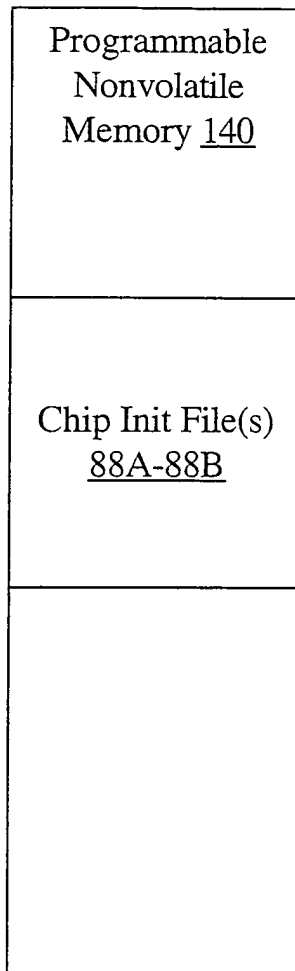


Fig. 12

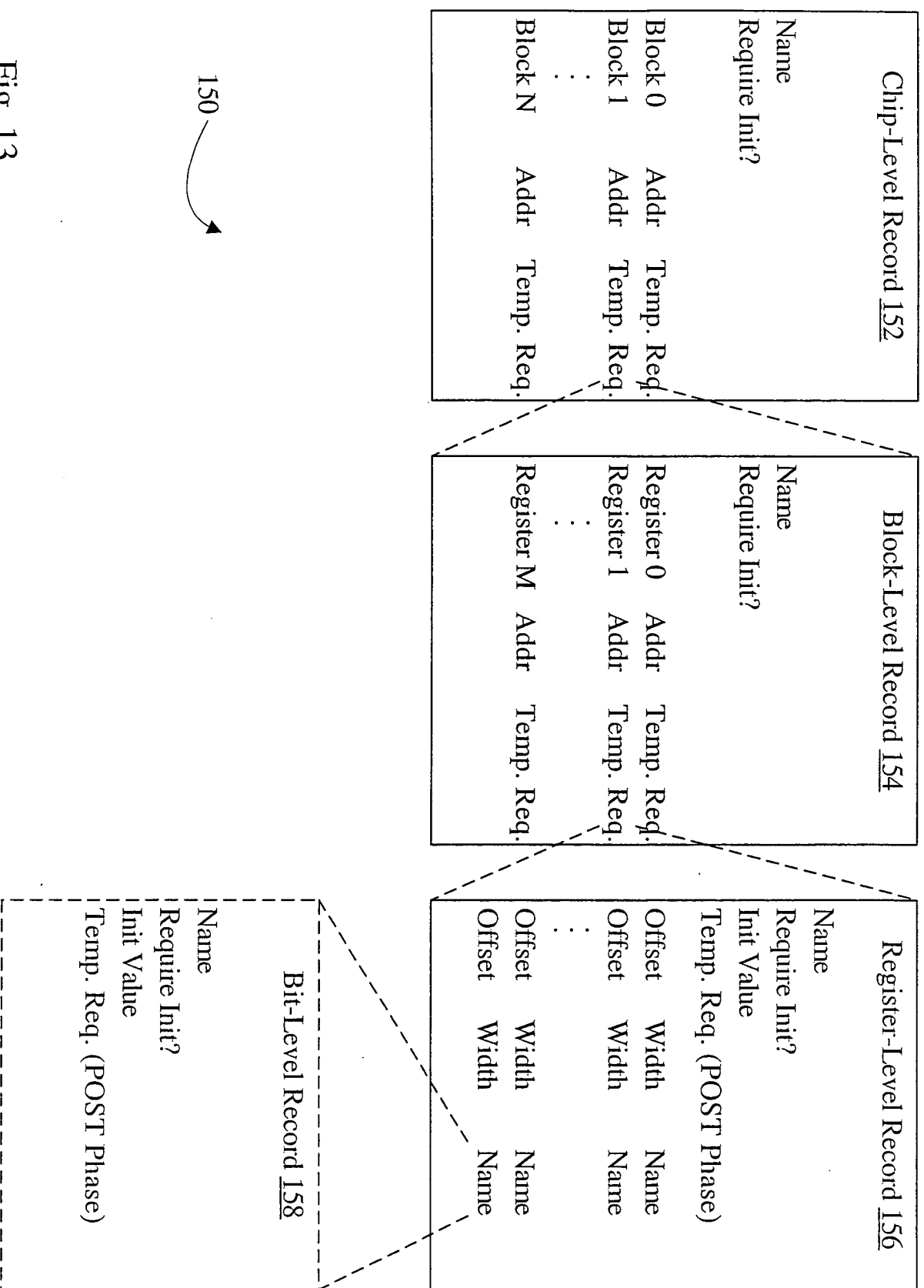


Fig. 13

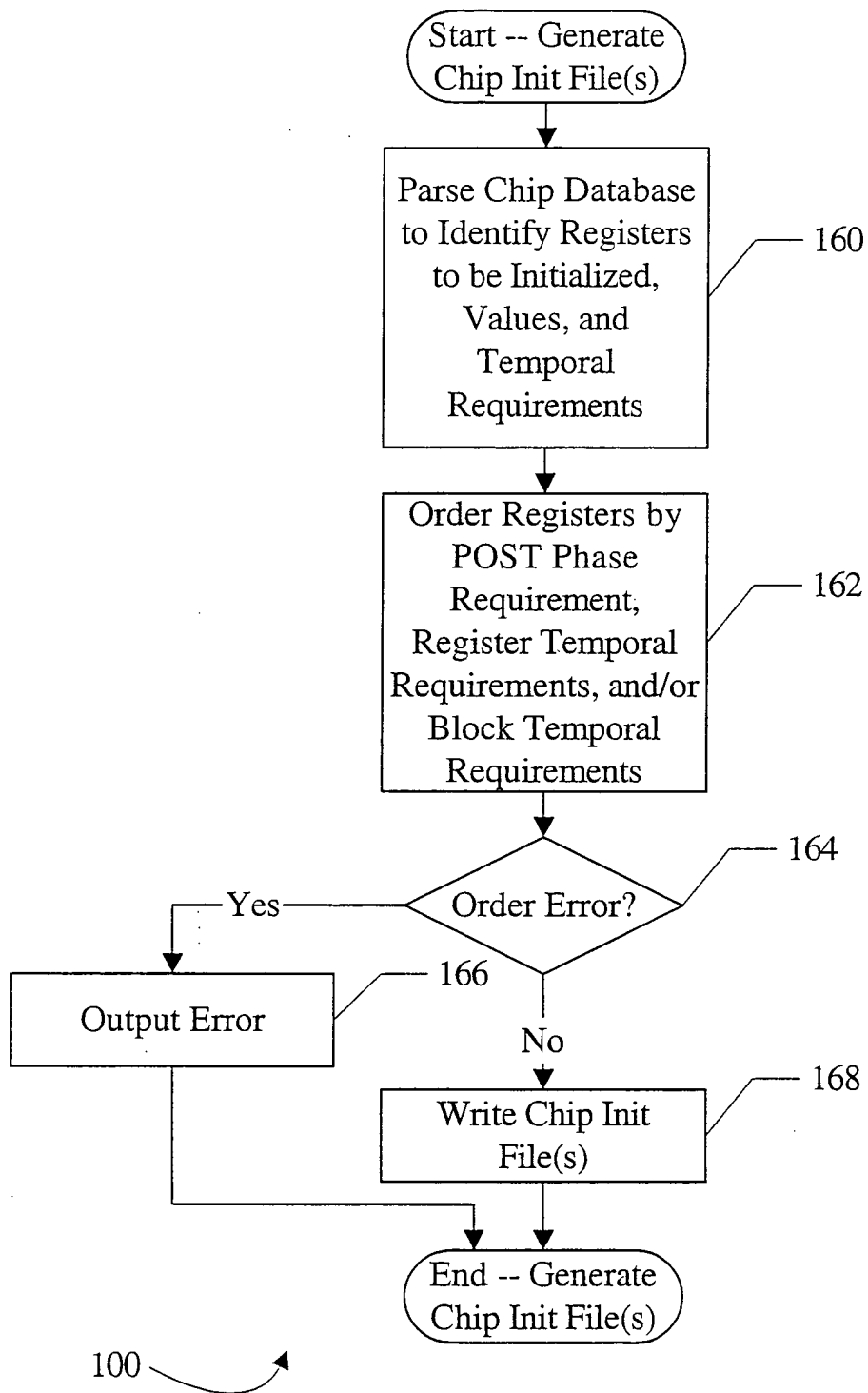


Fig. 14

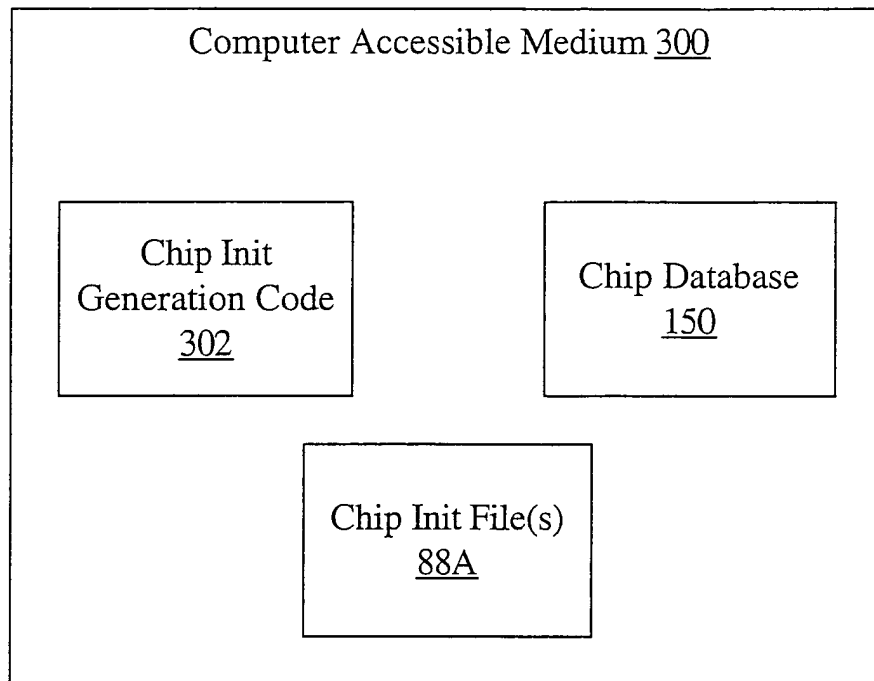


Fig. 15